



2133  
#2/7-21-9  
V. Jones

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Thurston, Andrew J.  
Assignee: Cisco Technology, Inc.  
Title: Automatic Generation Of Hardware Description Language Code For  
Complex Polynomial Functions  
Serial No.: 09/822,713 Filing Date: March 30, 2001  
Examiner: Unassigned Group Art Unit: 2133  
Docket No.: M-8319 US

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San Jose, California  
June 29, 2001

JUL 10 2001

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Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the  
accompanying PTO Form-1449 are called to the attention of the Examiner for the above  
patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

I hereby certify that this correspondence is being deposited with  
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20231, on June 29, 2001.

Rita M. Wisor

Attorney for Applicant(s)

6/29/01

Date of Signature

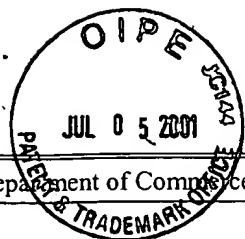
Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office						Atty Docket No.		Serial No.	
						M-8319 US		09/822,713	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Applicant(s)			
(Use several sheets if necessary)						Thurston, Andrew J.			
						Filing Date		Group	
						March 30, 2001		2133	
U.S. Patent Documents									
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA								
	AB								
	AC								
	AD								
	AE								
	AF								
	AG								
	AH								
	AI								
	AJ								
	AK								
Foreign Patent Documents									
							Translation		
		Document	Date	Country	Class	Subclass	Yes	No	
	AE								
	AM								
	AN								
	AO								
	AP								
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
	AR	Christian Schuler, "Code Generation Tools For Hardware Implementation Of FEC Circuits", 6 <sup>th</sup> International Conference on Electronics, Circuits And Systems ICECS '99, Paphos, Cyprus, Greece, Sept. 5-8, 1999.							
	AR								
	AS								
Examiner			Date Considered						
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>									

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